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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicants: Epps, Garry P.; Laor, Michael
Assignee: Cisco Technology, Inc.
Title: Pipelined Packet Switching And Queuing Architecture
Serial No.: 09/503,552 Filing Date: 02/14/2000
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-7413 US

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San Jose, California

May 16, 2000

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT
37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or

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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 5/16, 2000.

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5/16/00
Date of Signature

Respectfully submitted,



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